

REMARKS

I. Introduction

Claims 1-8 and 32-40 are pending in the above application.

Claims 1-8 and 32-40 stand allowed.

Claim 48 is not indicated as being rejected and, hence is presumed to be allowed.

II. Amendments

Claims 9, 12-31 and 41-48 have been cancelled without prejudice or disclaimer.

Claims 49-60 are newly added.

No new matter has been added.

III. New Claims 49-60

New claims 49 and 59 are independent claims from which claims 50-58 and 60 depend from, respectively. New claims 49-60 are believed to be patentable at least because independent claims 49 and 59 recite features which are not disclosed or suggested by the prior art.

As stated in the Reasons for Allowance, the prior art does not teach or suggest a graphics processor comprising a graphics command storing section including a first data storing means and a second data storing means having a check address, and when an address of a graphics command being read out matches a predetermined check address, the graphics processor comparing the priority of storing a graphics command to the first data storing means (claim 49 recites the first storage means as being included in the graphics command storing section) with the priority of the data transfer operation. Accordingly, claim 49, and claims 50-58 which depend on claim 49, are believed to be allowable over the prior art of record at least for the same reasons a prior to being amended.

Regarding claim 59, the prior art does not teach or suggest estimating a data processing amount by pre-decoding data transferring from the interface unit to the memory, and when the estimated data processing amount exceeds a predetermined amount, increasing a priority of the graphics command supply operation from the memory to the display data generation section.

U.S. Patent 6,600,492 to Shimoura merely discloses, estimating a time in which an internal buffer in the video input circuit 330 will be filled up with input video data, and judging a priority level of the video input circuit 330 to use the internal bus 110 for accessing the memory unit 700. Shimoura does not disclose or suggest estimating a data processing amount by pre-decoding data transferring from the interface unit to the memory. Accordingly, as Shimoura does not each and every element of claim 41, Shimoura does not anticipate amended claim 41, nor claim 43 which depends on amended claim 41.

IV. Conclusion

Having fully responded to all matters raised in the Office Action, Applicants submit that all claims are in condition for allowance, an indication for which is respectfully solicited.

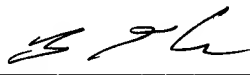
If there are any outstanding issues that might be resolved by an interview or an Examiner's amendment, the Examiner is requested to call Applicants' attorney at the telephone number shown below.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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